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ATTORNEY DOCKET NO.
004578.1147

PATENT APPLICATION
Serial No. 10/028,009 #3
mdj
10-18-02

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Application of: DOUCETTE, Paul E., et al.
Serial No.: 10/028,009
Filing Date: December 21, 2001
Group No.: Unknown
Examiner: Unknown
Title: METHOD AND APPARATUS FOR PROCESSING
SIGNALS IN AN ARRAY ANTENNA SYSTEM

Assistant Commissioner
for Patents
Washington, D.C. 20231

Dear Sir:

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on the date shown below.

Name Judith M. Braggart
Date: 12/24/2001

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SECOND INFORMATION DISCLOSURE STATEMENT Technology Center 2600

Pursuant to the provisions of 37 CFR §§ 1.56, 1.97 and 1.98, Applicants are enclosing copies of several documents which may be material to examination of the present application, each of which is listed on enclosed Form PTO-1449. Each of the enclosed documents was cited during the examination of copending U.S. Serial No. 09/478,035 filed January 5, 2000. The copending application was cited in a prior Information Disclosure Statement filed in the present application on December 21, 2001.

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PATENT APPLICATION
Serial No. 10/028,009

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Favorable consideration of the present application
is respectfully requested.

Respectfully submitted,
BAKER BOTTS L.L.P.
Attorneys for Applicants



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Reg. No. 30,222

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Date: January 24, 2002

Enclosures: Form PTO-1449, and one copy
of each listed document

PTO-1449		Application No. 10/028,009		Applicant(s) Paul E. Doucette, et al.			
Information Disclosure Citation In an Application		Docket Number 004578.1147	Group Art Unit Unknown	Filing Date December 21, 2001			
MAR 08 2002 U.S. PATENT DOCUMENTS							
	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
A	3,887,918	06/03/1975	Bailey				
B	4,688,045	08/18/1987	Knudsen				
C	4,749,995	06/07/1988	Hopwood, et al.				
D	4,886,069	12/12/1989	O'Donnell				
E	5,084,708	1/28/1992	Champeau, et al.				
F	5,223,843	06/29/1993	Hutchinson				
G	5,271,034	12/14/1993	Abaunza				
H	5,414,433	05/09/1995	Chang				
I	5,461,389	10/24/1995	Dean				
J	5,764,187	06/09/1998	Rudish, et al.				
K	5,943,010	08/24/1999	Rudish, et al.				
L	6,031,485	02/29/2000	Cellai, et al.				
FOREIGN PATENT DOCUMENTS							
	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
M	2 130 798 A	06/06/1984	UK			--	
N	2 313 711 A	12/03/1997	UK			--	
O	WO-01/67548 A2	09/13/2001	PCT			--	
P							
Q							
DOCUMENT (Including Author, Title, Source, Pertinent Pages, and Date)							
R	Thompson, Rick et al., "An 8-Bit 2-Gigasample/Second A/D Converter Multichip Module for Digital Receiver Demonstration on Navy AN/APS-145 E2-C Airborne Early Warning Aircraft Radar", IEEE Trans on CPMT Part B, vol. 21, No. 4, 11/1998, pp. 447-462.						
S	Brinegar, C., et al. "Design of an Integrated RF Filter for the Direct Digitization Front End of a Dual GPS/GLONASS Software Radio Receiver", IEEE RAWCON '98 Proceedings, 8/1998, cover sheet and pp. 277-280.						
T	Ho, K.C., et al., "A Digital Quadrature Demodulation System", IEEE Trans. on AES, Vol. 32, No. 4, 10/1996, pp. 1218-1227.						
EXAMINER				DATE CONSIDERED			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.							
U.S. PATENT AND TRADEMARK OFFICE							

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MAR 08 2002 U.S. PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	U	6,052,085	04/18/2000	Hanson, et al.			
	V	6,141,371	10/31/2000	Holmes, et al.			
	W	6,191,735	02/20/2001	Schineller			
	X						
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	AA						
	BB						
	CC						
	DD						
	EE						
	FF						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO
	GG						
	HH						
	II						
	JJ						
	KK						
DOCUMENT (Including Author, Title, Source, Pertinent Pages, and Date)							
	LL	Brown, A. et al., "Digital Downconversion Test Results with a Broadband L-Band GPS Receiver", IEEE Digital Avionics Systems Conf. 10/1994, cover sheet and pp. 426-431.					
	MM	Eerola, V. et al., "Direct Conversion Using Lowpass Sigma-Delta Modulation", Proc. IEEE Int'l Symposium on Circuits and Systems, 5/1992, cover sheet and pp. 2653-2656.					
	NN	Steyskal, Hans, "Digital Beamforming at Rome Laboratory", Microwave Journal, U.S. Horizon House, Dedham, vol. 176, No. 2, 02/1996, pages 100, 104, 107-8, 110, 113-4, 116, 119-120, 122, -124, 126.					
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U.S. PATENT DOCUMENTS					
		DOCUMENT NO.	DATE	NAME	CLASS
	OO				
	PP				
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	VV				
	WW				
	XX				
	YY				
	ZZ				
FOREIGN PATENT DOCUMENTS					
		DOCUMENT NO.	DATE	COUNTRY	CLASS
	AAA				
	BBB				
	CCC				
DOCUMENT (Including Author, Title, Source, Pertinent Pages, and Date)					
	DDD	U.S. Serial No. 09/519,069, filed March 3, 2000, entitled "DIGITAL PHASED ARRAY ARCHITECTURE AND ASSOCIATED METHOD", by inventor Gary A Frazier, text pages 1-31 and 3 drawing sheets. (Attorney Docket RAYT:009, case 37323).			
	EEE	Wong, A.C.C., "Radar Digital Beamforming", Standard Telecommunication Laboratories, Harlow, Essex, U.K., Military Microwaves Conference 1982, 10/20/83, cover sheet and pp. 287-294.			
	FFF	Caputi, William J., "Stretch: A Time-Transformation Technique", IEEE Transactions on Aerospace and Electronic Systems, Vol. AES-7, No. 2, March 1971, pp. 269-278.			
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